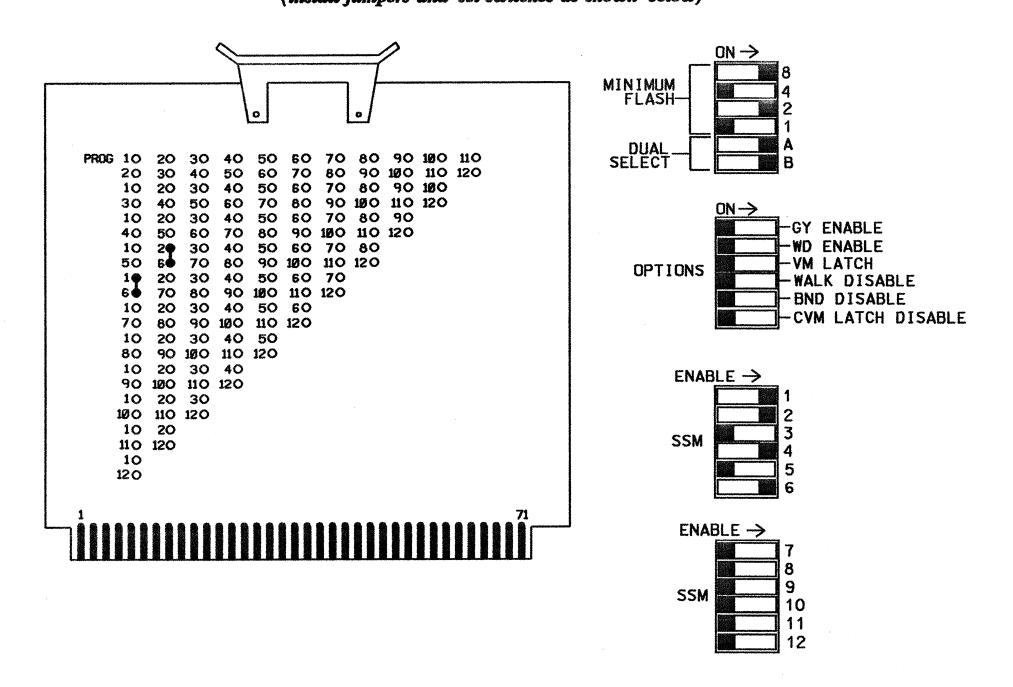
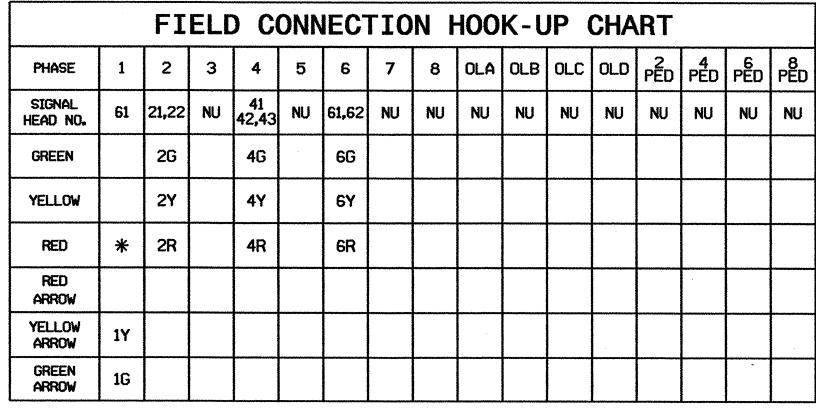
(install jumpers and set switches as shown below)



NOTES

- 1. TO PREVENT "FLASH-CONFLICT" PROBLEMS, WIRE ALL UNUSED PHASES AND OVERLAPS TO FLASH RED. VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
- 2. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED LOAD SWITCH RED OUTPUTS 3,5,7,8,9,10,11 AND 12 TO LOAD SWITCH AC+ BY INSERTING A JUMPER PLUG IN THE UNUSED LOAD SWITCH SOCKET FROM PIN 1 (LS AC+) TO PIN 3 (RED OUT). MAKE SURE ALL FLASH TRANSFER RELAYS ARE IN PLACE.
- 3. PROGRAM CONTROLLER TO START UP IN PHASES 2 AND 6 GREEN.
- 4. SET POWER-UP FLASH TIME TO 10 SECONDS AND IMPLEMENT ON THE CONFLICT MONITOR. SET CONTROLLER POWER-UP FLASH TIME TO 0 SECONDS.
- 5. ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
- 6. WIRE DETECTORS IN ACCORDANCE WITH THE MANUFACTURER'S INSTRUCTIONS TO ACCOMPLISH THE DETECTION SCHEMES SHOWN ON THE SIGNAL DESIGN PLANS.
- 7. SET ALL DETECTOR UNIT CHANNELS TO "PRESENCE" MODE.
- 8. PROGRAM PHASES 2 AND 6, ON CONTROLLER UNIT, FOR VOLUME DENSITY OPERATION.

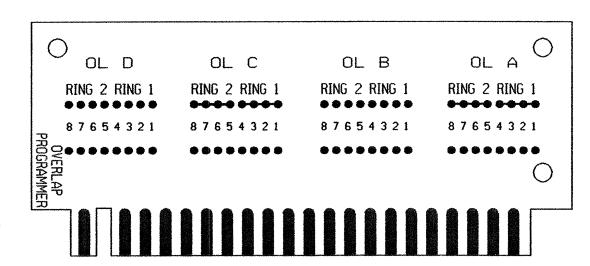
PROJECT REFERENCE NO. SHEET NO. R-0513C SIG. 7



NU = NOT USED

* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

NEMA OVERLAP CARD

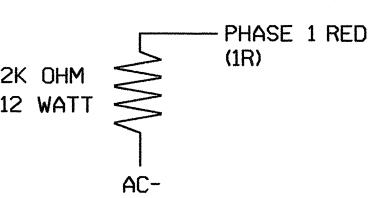


OVERLAP CARD SHALL BE COMPLETELY BLANK (NO OVERLAPS)

EQUIPMENT INFORMATION

CONTROLLEREAGLE EPAC300 (M10)
CABINETEAGLE TF4016TNC01
CABINET MOUNTBASE
LOADBAY POSITIONS16
LOAD SWITCHES USED1,2,4,6
PHASES USED
OVERLAP ANOT USED
OVERLAP BNOT USED
OVERLAP CNOT USED
OVERLAP DNOT USED

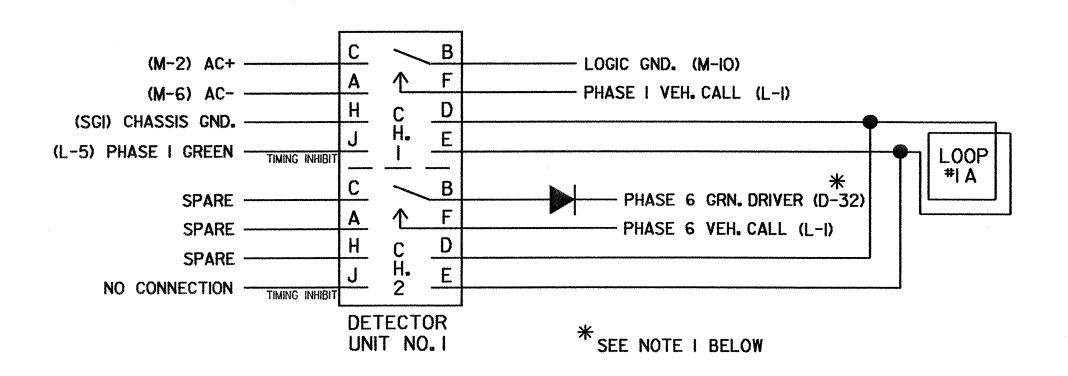
LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THIS RESISTOR IS TO LOAD THE CHANNEL RED MONITOR INPUT IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON PHASES THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

SPECIAL DETECTOR WIRING DETAIL

(WIRE AS SHOWN)



NOTES:

- I. TERMINAL DESIGNATIONS SHOWN ARE LOCATED ON THE DETECTOR PANEL, EXCEPT FOR PHASE GREEN DRIVER, WHICH IS LOCATED ON THE BACK PANEL.
- 2. DIODE IS VALUED AT 600V PIV, I AMP MINIMUM. (RECOMMENDED PART NO. I N4005)

TYPICAL CONNECTION CHART FOR DETECTORS

PIN FUNCTION TERMINATION

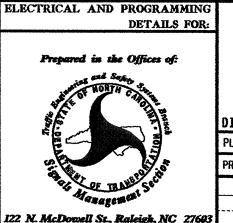
PIN FUNCTION	TERMINATION
AC+	AC+
AC-	AC-
CHASSIS GROUND	CHASSIS GROUND
LOOP INPUT	LOOP
LOOP INPUT	LOOP
RELAY NORMALLY OPEN	VEHICLE CALL INPUT
RELAY COMMON	LOGIC GROUND
TIMER INHIBIT	ASSOCIATED PHASE GREEN

NOTES:

- I. THE TIMER INHIBIT WIRE SHALL BE CONNECTED TO THE ASSOCIATED PHASE GREEN LOAD SWITCH OUTPUT WHEN ONLY DELAY OPERATION IS REQUIRED UNLESS OTHERWISE SPECIFIED BY THE LOOP AND DETECTOR UNIT INSTALLATION CHART.
- 2. IF EXTEND OPERATION IS REQUIRED, THE DELAY INHIBIT WIRE SHALL NOT BE CONNECTED.

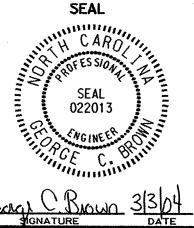
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0489 T1 DESIGNED: FEBRUARY 2004 SEALED: 2/19/04 REVISED:

TEMPORARY SIGNAL 1 - PHASE I



US 74 BUSINESS AT I-95 SB RAMPS

DIVISION 6 ROBESON COUNTY LUMBERTON
PLAN DATE: FEBRUARY 2004 REVIEWED BY:
PREPARED BY: WILLIAM HAIRSTON REVIEWED BY:
REVISIONS INIT. DATE



SIG. INVENTORY NO. 06-0489

MAR-2004 08:33